

DT05 Rec'd PCT/PTO 2 1 DEC 2004

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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APPLICANT	Richard Michael Taylor
TITLE	MICROPROCESSOR INSTRUCTION EXECUTION METHOD FOR EXPLOITING PARALLELISM
FILING DATE	Herewith
PCT INTERNATIONAL APPLICATION NO.	PCT/GB03/002820
INTERNATIONAL FILING DATE	June 30, 2003

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**PRELIMINARY AMENDMENT**

Dear Sir:

Preliminary to a first office action on the merits of this US national phase application, based upon a PCT application previously previously amended on July 1, 2004, please amend the specification as follows: